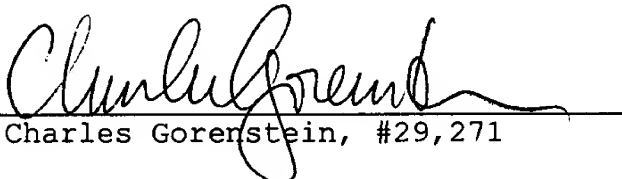


- ☐ Petition for () month(s) extension of time pursuant to 37 C.F.R. §§ 1.17 and 1.136(a). \$0.00 for the extension of time.
- ☒ No fee is required.
- ☐ Check(s) in the amount of \$0.00 is(are) enclosed.
- ☐ Please charge Deposit Account No. 02-2448 in the amount of \$0.00. This form is submitted in triplicate.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. § 1.16 or under 37 C.F.R. § 1.17; particularly, extension of time fees.

Respectfully submitted,

BIRCH, STEWART, KOLASCH & BIRCH, LLP

By 
Charles Gorenstein, #29,271

CG/CTB/mpe
1248-0538P

P.O. Box 747
Falls Church, VA 22040-0747
(703) 205-8000

ATTACHMENT

(Rev. 09/27/01)



#9/c
10/17/2
PATENT
1248-0538P
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Eiji YANAGAWA et al. Conf. No.: 4277
Appl. No.: 09/820,671 Group: 2815
Filed: March 30, 2001 Examiner: S. Clark
For: SEMICONDUCTOR DEVICE & METHOD OF
MANUFACTURING THE SAME

SECONDARY SUPPLEMENTAL AMENDMENT

Honorable Commissioner for Patents
Washington, D.C. 20231

September 11, 2002

Sir:

Further to our previous Responses filed August 29, 2002 and July 1, 2002 and in response to the Office Action dated April 1, 2002, the following secondary supplemental amendment is respectfully submitted in connection with the above-identified application.

IN THE CLAIMS:

Please replace claims 7-9 and 22 with the following amended claims: Please note that claims 7-9 and 22 are presented below in their amended form. They are further presented as an Attachment to the Amendment whereby the amendments to the claims are outlined using the conventional method of bracketing and underlining.

Sub
D1
C1
7. (Three Times Amended) A method of manufacturing a semiconductor device, comprising the steps of:

(i) securing a semiconductor element having an integrated circuit to a board so that the semiconductor element is maintained in a level position;